



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **HAYAKAWA, Akinori, et al.**

Group Art Unit: **Unassigned**

Serial No.: **10/690,469**

Examiner: **Unassigned**

Filed: **October 22, 2003**

**FOR: PHOTOSEMICONDUCTOR DEVICE, METHOD FOR FABRICATING
PHOTOSEMICONDUCTOR DEVICE AND METHOD FOR DRIVING
PHOTOSEMICONDUCTOR DEVICE**

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 CFR 1.97(b)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

December 17, 2003

Sir:

The attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached Form PTO-1449. One copy of each of these documents is attached.

No fee or certification is required in connection with this Information Disclosure Statement, since it is being submitted prior to the issuance of a first official action on the merits or expiration of the three month period following the filing date or the entry of the national stage of the above-captioned application.

The above information is presented so that the Patent and Trademark Office can determine any materiality thereof to the claimed invention. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the documents cited in the attached Form PTO-1449 be made of record therein and appear on the first page of any patent to issue therefrom.

The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

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PATENT TRADEMARK OFFICE

Enclosures: PTO-1449 and references (9)



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	Applicant(s): HAYAKAWA, Akinori, et al.	
	Filing Date: October 22, 2003	Group Art Unit: Unassigned

U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA	5,048,049	Amann	09/10/1991	372	96	
_____	AB						

FOREIGN PATENT DOCUMENTS

		Document No.	Date	Country	Translation (Yes or No)
_____	AC	7-326820	12/12/1995	Japan	Abstract
_____	AD	7-131121	05/19/1995	Japan	Abstract
_____	AE	7-135368	05/23/1995	Japan	Abstract
_____	AF	6-97591	04/08/1994	Japan	Abstract
_____	AG	2002-6352	01/09/2002	Japan	Abstract

OTHER DOCUMENTS

_____	AH	Schmidt et al., "Design and Realization of a Buried-Heterostructure Turnable-Twin-Guide Laser Diode with Electrical Blocking Regions", <u>IEEE JOURNAL OF QUANTUM ELECTRONICS</u> , Vol. 35, No. 5, May 1999, pages 794-802.
_____	AI	Bouda et al., "Compact High-Power Wavelength Selectable Lasers for WDM Applications", <u>Optical Fiber Communication Conference, 2000, Technical Digest</u> , Vol. 1, pages 178-180.
_____	AJ	Mason et al., "Design of Sampled Grating DBR Lasers with Integrated Semiconductor Optical Amplifiers", <u>IEEE PHOTONICS TECHNOLOGY LETTERS</u> , Vol. 12, No. 7, July 2000, pages 762-764.
Examiner		Date Considered